

TFX

Electronic Filing System (EFS) Data
Electronic Patent Application Submission
USPTO Use Only

EFS ID: 61288

Application ID: 10749342



Title of Invention:

CIRCUIT FOR AUTO-CLAMPING
INPUT PINS TO A DEFINITE
VOLTAGE DURING POWER-UP OR
RESET

First Named Inventor: Philip Ng

Domestic/Foreign Application: Domestic Application

Filing Date: 2003-12-31

Effective Receipt Date: 2004-05-20

Submission Type: Information Disclosure
Statement

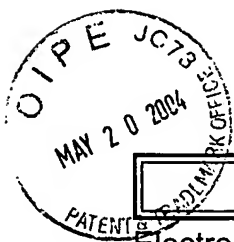
Filing Type:

Confirmation number: 8208

Attorney Docket Number: ATM-299

Total Fees Authorized:


Digital Certificate Holder: cn=Thomas Schneck,ou=Registered Attorneys,ou=Patent and Trademark
Office,ou=Department of Commerce,o=U.S. Government,c=US
Certificate Message Digest: 7aeaf849de2e47e98ca8520ae73b8cd91a4e4cd2

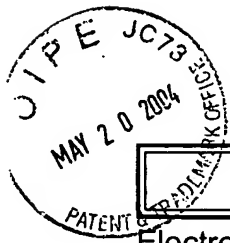


TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	CIRCUIT FOR AUTO-CLAMPING INPUT PINS TO A DEFINITE VOLTAGE DURING POWER-UP OR RESET									
Application Number: 10/749342 										
Date: 2003-12-31										
First Named Applicant: Philip S. Ng										
Confirmation Number: 8208										
Attorney Docket Number: ATM-299										
<p>I hereby certify that the use of this system is for OFFICIAL correspondence between patent applicants or their representatives and the USPTO. Fraudulent or other use besides the filing of official correspondence by authorized parties is strictly prohibited, and subject to a fine and/or imprisonment under applicable law.</p> <p>I, the undersigned, certify that I have viewed a display of document(s) being electronically submitted to the United States Patent and Trademark Office, using either the USPTO provided style sheet or software, and that this is the document(s) I intend for initiation or further prosecution of a patent application noted in the submission. This document(s) will become part of the official electronic record at the USPTO.</p>										
<table border="1"><thead><tr><th>Submitted by:</th><th>Elec. Sign.</th><th>Sign. Capacity</th></tr></thead><tbody><tr><td>Thomas Schneck Registered Number: 24518</td><td>/thomas schneck/</td><td>Attorney</td></tr></tbody></table>			Submitted by:	Elec. Sign.	Sign. Capacity	Thomas Schneck Registered Number: 24518	/thomas schneck/	Attorney		
Submitted by:	Elec. Sign.	Sign. Capacity								
Thomas Schneck Registered Number: 24518	/thomas schneck/	Attorney								
<table><tr><td>Documents being submitted</td><td>Files</td></tr><tr><td>us-ids</td><td>ATM299ids-usidst.xml</td></tr><tr><td></td><td>us-ids.dtd</td></tr><tr><td></td><td>us-ids.xsl</td></tr></table>			Documents being submitted	Files	us-ids	ATM299ids-usidst.xml		us-ids.dtd		us-ids.xsl
Documents being submitted	Files									
us-ids	ATM299ids-usidst.xml									
	us-ids.dtd									
	us-ids.xsl									
Comments										



ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of
Invention

CIRCUIT FOR AUTO-CLAMPING INPUT PINS TO A
DEFINITE VOLTAGE DURING POWER-UP OR RESET

Application Number: 10/749342



Confirmation Number: 8208

First Named Applicant: Philip Ng

Attorney Docket Number: ATM-299

Art Unit: 2816

Search string: (2017813 or 5159206 or 5500611 or 6160429
or 6335648 or 6509768 or 6650155 or
20030214337).pn.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	2017813	1991-05-21	Galbraith et al.	A1	307	475
	2	5159206	1992-10-27	Tsay et al.	A1	307	272.3
	3	5500611	1996-03-19	Popat et al.	A1	326	87
	4	6160429	2000-12-12	Morrill	A1	327	143
	5	6335648	2002-01-01	Matsuhita	A1	327	198
	6	6509768	2003-01-21	Polizzi et al.	A1	327	143
	7	6650155	2003-11-18	Nguyen et al.	A1	327	143

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
	1	20030214337	2003-11-20	Miyagi	A1	327	215

Signature

Examiner Name

Date

